

Appl. No. 09/669,396  
Amdt. dated July 19, 2004  
Reply to Office Action of May 26, 2004

### AMENDMENTS TO THE CLAIMS

The listing of claims below replaces all prior versions, and listings, of claims:

1           1.       (Currently Amended) A network comprising:  
2                   a network fabric comprising at least two switches and a plurality of links,  
3       each link connected to a least one switch of the at least two switches;  
4                   a first N\_Port connected to a link of the network fabric;  
5                   a second N\_Port connected to a link of the network fabric;  
6                   wherein there exists a first path and a second path from the first N\_Port to  
7       the second N\_Port through the network fabric;  
8                   wherein network traffic in a first direction from the first N\_Port to the  
9       second N\_Port is automatically distributed between the first path and the second path by  
10      one of the at least two switches such that frames transmitted in the first direction and  
11      related to any single exchange are transmitted over the same path of the first and second  
12      paths yet frames transmitted in the first direction and related to different but overlapping  
13      exchanges need not follow the same path;  
14                  wherein frames are routed by at least one routing table, the routing table  
15      having inputs comprising a hash function of a destination identifier of the a frame header  
16      and at least one bit of the an originator exchange identifier and the hash function further  
17      comprises a first hash sub-function of at least one bit of the destination identifier and  
18      having an output, a second hash sub-function of the at least one bit of the originator  
19      exchange identifier and having an output, and a concatenation operation of the output of  
20      the first hash sub-function with the output of the second hash sub-function.

1           2.       (Original) The network of Claim 1, wherein the frames related to the any  
2       single exchange are identified by a switch as belonging to the single exchange through  
3       fields of a frame header comprising an originator exchange identifier field.

1           3.       (Previously Presented) The network of Claim 1, wherein the at least one  
2       routing table is located within a switch of the at least two switches.

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1           4.       (Previously Presented) The network of Claim 1, wherein the network  
2       comprises a switched Fibre Channel fabric.

1           5.       (Previously Presented) The network of Claim 1, wherein the hash function  
2       has an input further comprising a field selected from the group consisting of a source  
3       identifier field of the frame header and an incoming port number on which the frame was  
4       received by the switch.

1           6.       (Previously Presented) The network of Claim 1, wherein the routing table  
2       produces an index to a second table that provides an outgoing port identifier for the  
3       switch.

1           7.       (Previously Presented) The network of Claim 1, wherein a load-balancing  
2       task of the network updates the at least one routing table to alter a distribution of  
3       exchanges among paths.

1           8.       (Cancelled)

1           9.       (Previously Presented) The network of Claim 1, wherein the second hash  
2       sub-function is a bit select operation.

1           10.      (Previously Presented) The network of Claim 1, wherein the hash function  
2       has inputs further comprising an input selected from the group consisting of an incoming  
3       port identifier on which the frame was received and at least one bit of a source identifier  
4       field of the frame header.

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1           11.     (Currently Amended) A program product for distributing network traffic  
2     between a first N\_Port of a network and a second N\_Port of a network, the network  
3     having a plurality of paths for frames from the first N\_Port to the second N\_Port and at  
4     least one switch, the program product operable upon said switch and comprising  
5     computer-readable code for:  
6                 maintaining a routing table, the routing table indexed by an output of a  
7     hash function of inputs comprising a destination identification field and an originator  
8     exchange identifier field of a header of a frame;  
9                 causing the routing table to be accessed upon receipt of a frame, the  
10    routing table coupled to determine a selected port for transmission of the frame; and  
11                 causing the frame to be transmitted on the selected port,  
12                 wherein the hash function further comprises a first hash sub-function of at  
13    least one bit of the destination identification field and having an output, a second hash  
14    sub-function of the at least one bit of the originator exchange identifier field and having  
15    an output, and a concatenation operation of the output of the first hash sub-function with  
16    the output of the second hash sub-function.

1           12.     (Previously Presented) The program product of Claim 11, wherein the  
2     hash function has inputs further comprising an input selected from the group consisting  
3     of a source identifier field of the frame header and an identity of a switch port upon  
4     which the frame was received.

1           13.     (Original) The program product of Claim 11, wherein the routing table is  
2     coupled to determine a selected port by providing an index to a second table that provides  
3     a selected port identifier.

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1           14.     (Currently Amended) A switch for a network capable of distributing  
2 frames received on a first port over a plurality of ports, the switch comprising  
3                   a plurality of ports including a first port, the first port capable of receiving  
4 a frame;  
5                   a routing table capable of determining a port of the plurality of ports for  
6 forwarding a received frame based upon an address;  
7                   a hash function generator capable of generating an address for the routing  
8 table based upon information comprising a destination identification field and at least one  
9 bit of an originator exchange identifier field of a header of the received frame;  
10                  a processor for maintaining the routing table; and  
11                  apparatus for receiving a frame and for passing a received frame to the  
12 port determined by the routing table,  
13                  wherein ~~the~~ a hash function provided by the hash function generator  
14 ~~further~~ comprises a first hash sub-function of at least one bit of the destination  
15 identification field and having an output, a second hash sub-function of the at least one  
16 bit of the originator exchange identifier field and having an output, and a concatenation  
17 operation of the output of the first hash sub-function with the output of the second hash  
18 sub-function.

1           15.     (Previously Presented) The switch of Claim 14, wherein the hash function  
2 generator is capable of generating an address for the routing table based upon information  
3 further comprising an identifier selected from the group consisting of a source identifier  
4 field of the header of the received frame and a N\_Port identifier of the switch port on  
5 which the frame is received.

1           16.     (Original) The switch of Claim 14, wherein the hash function generator  
2 further comprises devices to perform the hash function of destination identification field  
3 and at least one bit of an originator exchange identifier field of the header of the received  
4 frame, and the routing table comprises a memory capable of being addressed by the  
5 address generated by the hash function.

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- 1           17.     (Previously Presented) The switch of Claim 16, wherein the memory of
- 2     the routing table is implemented by at least one RAM, the RAM being writable by the
- 3     processor and coupled to be addressed through a multiplexor capable of providing a
- 4     RAM address from the group of addresses comprising an address generated by the
- 5     processor and the address generated by the hash function.